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\$BEGIN RMSMSCMAC,000, 1980, 1982, 1984 BY
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Facility:

RMS-32

Abstract:

RMS internal macros

Modified By:

V03-007 DAS0001 David Solomon 13-Feb-1984 Remove temporary (and now obsolete) RMS Recovery macros.

V03-006 SHZ0001 Stephen H. Zalewski 21-Apr-1983 Remove obsolete macros.

V03-005 KBT0448 Keith B. Thompson 5-Dec-1982 Remove the Scachec macro

V03-004 JWH0163 Jeffrey W. Horn 21-Dec-1982 Make references to the tracepoint page be offset from a SHELL global variable (PIO\$A\_TRACE) rather than the top of the process IO impure area.

V03-003 KPL0001 Peter Lieberwirth 26-0ct-1982 Add some macros for RMS recovery. (Some of these are only temporary.)

V03-002 RAS0089 7-Jun-1982 Ron Schaefer Delete the incorrect \$QUAD\_ALIGN macro.

V03-001 RAS0078 17-Mar-1982 Ron Schaefer Fix the \$BEGIN macro for V3.

```
RMSMSCMAC.MAR; 1
         .NLIST
   macro to generate a global symbol and entry mask
.MACRO SENTRY P1, MASK
         SDPSECT
         . IF
                  NB <MASK>
P1::
         .WORD
                  ^M<R2,R3,R4,R5,R6,R7,R8,R9,R10,R11>
         .IFF
         P1 = -2
         .ENDC
.ENDM
         SENTRY
   a test system macro to:

    generate a global symbol if $$rmstest bit 0 on
    increment a global location if $$rmstest bit 1 on

.MACRO $TSTPT P1
                  NE SSRMSTEST&1
         .IF
$$TP$'P1::
         .ENDC
         . IF
                  NE SSRMSTEST82
         INCL
                  a#PIO$A_TRACE+TPT$L_'P1
        .ENDC
$TSTPT
.ENDM
   macro to set a single bit
.MACRO SSB POS, BAS, ?L
         BBSS
                 POS, BAS, L
.ENDM
        SSB
   macro to clear a single bit
.MACRO CSB POS,BAS,?L
         BBCC
                  POS, BAS, L
.ENDM
         CSB
   module begin macro
```

.MACRO \$BEGIN MODNAM, IDNUM, PSNAME, TITL, PSATR

```
RMSMSCMAC.MAR:1
           .TITLE MODNAM TITL
           .IDENT /VO4-'IDNUM/
.MACRO SDPSECT
           .PSECT PSNAME, GBL, PIC, NOWRT, PSATR
.ENDM
           $DPSECT
           $$.PSECT_EP=0
$$RMSTEST=26
                                                        ; get test point increment, rmsbug,
; and optional debug code
           $$RMS_UMODE=4
$$RMS_TBUGCHK=8
$$RMS_PBUGCHK=16
.DSABL_DBG
           SBEGIN
.ENDM
    the Sbegin macro defines the following switches for rms assembly
           (bits in $$rmstest):
                                             include tstpt global symbols include tstpt counter increment
           bit 0
           bit 1
                      ($$rms_umode) assemble code for user mode
($$rms_tbugchk) include optional debugging code
($$rms_pbugchk) include 'semi-permanent' debugging code
           bit 2
bit 3
           bit 4
    hard error macros
.MACRO RMSPBUG CODE
           , IF
                      NE $$RMSTEST&$$RMS_PBUGCHK
           $FTLDEF
           $BUGDEF
                      RM$BUG
           JSB
```

BYTE

.ENDC

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JSB .BYTE

.ENDC

RMSTBUG

**RMSPBUG** 

**\$**FTLDEF

RMSTBUG CODE

.ENDM

.ENDM

.MACRO

CODE

RM\$BUG

CODE

NE \$\$RMSTEST&\$\$RMS\_TBUGCHK

.ENDM

.MACRO

.ENDM

.MACRO

.ENDM

```
RMSSTS NAM, ADDR
.IF DF RMS$ 'NAM
.IF EQ <RMS$ 'NAM&^XFFOO>
MOVZBL #<RMS$_'NAM&^XFF>, ADDR
.MACRO
           .IFF
          MOVZWL #<RMS$_'NAME^XFFFF>,ADDR
           .ENDC
           .MEXIT
           .IFF
          MOVZWL #<RMS$_'NAM&^XFFFF>,ADDR
           .ENDC
.ENDM
          RMSSTS
```

RMSERR\_WORD NAM ... WORD RMSS\_'NAM&^XFFFF

RMSERR\_WORD

macro to store rms error code

RMSSUC NAM=SUC,ADDR=RO

#1,ADDR

IDN <NAM><SUC>

RMSSTS NAM, ADDR

RMSSTS NAM, ADDR

.MACRO RMSERR NAM, ADDR=RO

RMSERR

. 1F

MOVL . IFF

.ENDC

RMSSUC

```
RMSMSCMAC.MAR; 1
```

```
.MACRO $CACHE VBN=R1,SIZE=R2,FLAGS=R3,ERR=
         . IF
                 IDN <VBN><WC>
        CLRL
        .IFF
                 DIF <VBN><R1>, MOVL VBN,R1
        .ENDC
        . IF
                 IDN <$1ZE><#0>
        CLRL
        .IFF
                 DIF <SIZE><R2>, MOVZWL SIZE,R2
        .ENDC
        . IF
                 DIF <FLAGS><R3>
        . IIF
                 IDN <FLAGS><#0>, CLRL R3
DIF <FLAGS><#0>, $CSHFLAGS <FLAGS>
        .IIF
        .ENDC
                 RMSCACHE
        BSBW
        .11F
                 NB <ERR>, BLBC RO, ERR
        SCACHE
.ENDM
.MACRO
        $CSHFLAGS FLAGS
        $CSHDEF
        $$.TMP=0
        .IRP
                X,<FLAGS>
        $$.TMP=$$.TMP!CSH$M_'X
        .ENDM
                EQ $5.TMP
        . IF
        CLRL
        .IFF
        .IIF
                 NE $$.TMP&CSH$M_NOBUFFER, $$.TMP=$$.TMP!CSH$M_NOREAD
                                          : nobuffer implies noread
        MOVL
                 #$$.TMP,R3
        .ENDC
.ENDM
        SCSHFLAGS
```

macro for specifying optional functions to be performed by rm\$rset. an in-line byte is generated specifying the options. a zero byte indicates no options desired.

for non-zero values.

anyfac (bit 5) set specifies that bits 0 - 2 are not to be used to check a fac bit.

bio (bit 3) set specifies that the file must be accessed for block i/o,; clear specifies that the file must not be accessed for block i/o

bits 6 and 7 are spares and must be zero.

.MACRO \$RABSET FAC=0,BIO=0,CFLG=0,ANYFAC=0
BSBW RM\$RSET
.BYTE <ANYFAC @ 5>!<CFLG @ 4>!<BIO @ 3>!FAC
.ENDM \$RABSET

the following macros are used to facilitate the gathering of relatively low usage code paths into separate psects in order to achieve higher normal-usage code locality. the advantage of this technique as opposed to using separate modules is that having all the relevant code in a single module improves readablity.

.MACRO SNEWPSECT PSNAME, PSATR

.SAVE .PSECT PSNAME, GBL, PIC, NOWRT, PSATR \$NEWPSECT

the \$psect\_restore macro merely returns to the previous psect.

.MACRO \$PSECT\_RESTORE PSNAME .RESTORE

.ENDM **\$PSECT\_RESTORE** 

.LIST

AH-BT13A-SE **EQUIPMENT** CORPORATION DIGITAL 031 V4.0 PROPRIETARY VAX/VMS CONFIDENTIAL AND III K EMELON TOTAL Part and and an arrangement of the second III. BETTER IN Rose TREE. THE STATE OF THE S